

AMENDMENTS TO THE CLAIMS:

Claim 1. (Canceled)

Claim 2. (Currently amended) The interposer of claim 5 ~~1~~, wherein the contact between the two adjacent buttons occurs when the two adjacent buttons are axially compressed above a predetermined threshold.

Claim 3. (Currently amended) The interposer of claim 5 ~~1~~, wherein the array of buttons is arranged in a regularly spaced array.

Claim 4. (Currently amended) The interposer of claim 5 ~~1~~, wherein the array of buttons is arranged in a custom arrangement based upon positions of the contacts on one of a chip carrier and a printed circuit board.

Claim 5. (Currently amended) An interposer comprising:
an array of buttons on a carrier having a proximity to each other that allows contact
between two adjacent buttons to occur when the two adjacent buttons are compressed above a
predetermined threshold ~~The interposer of claim 1~~, wherein a rate at which the radius of at least one of the buttons expands while being axially compressed slows at a first range of axial compression.

Claim 6. (Original) The interposer of claim 5, wherein the rate at which the radius of the two buttons expand while being axially compressed slows at a second range of axial

compression.

Claim 7. (Currently amended) The interposer of claim 5 ~~1~~, wherein the buttons comprise an elastomeric compound that has embedded metallic particles.

Claim 8. (Currently amended) The interposer of claim 5 ~~1~~, wherein the buttons comprise a deformable material.

Claim 9. (Original) The interposer of claim 8, wherein the buttons comprise a deformable metal.

Claim 10. (Original) The interposer of claim 9, wherein the buttons comprise soft metal balls.

Claim 11. (Previously presented) A chip package comprising:

a chip having a first surface and a second surface;

a printed circuit board having a first surface and a second surface, wherein the first surfaces are closer to each other than the second surfaces; and

an interposer having an array of buttons between said chip and said printed circuit board, wherein said first surfaces face each other and said second surfaces face each other.

Claim 12. (Original) The package of claim 11, wherein at least one button of the array of buttons is compressed between said first surfaces.

Claim 13. (Original) The package of claim 12, wherein said at least one button contacts an adjacent button in the array of buttons.

Claim 14. (Original) The package of claim 12, wherein at least one other button of the array of buttons is compressed between said second surfaces.

Claim 15. (Original) The package of claim 14, wherein said at least one button of the array of buttons comprises a plurality of buttons compressed between said first surfaces and surrounding said at least one other button of the array of buttons compressed between said second surfaces.

Claim 16. (Original) The package of claim 15, wherein the plurality of buttons compressed between said first surfaces are compressed above a predetermined threshold such that adjacent buttons of the plurality of buttons contact each other.

Claim 17. (Original) The package of claim 16, wherein the plurality of buttons compressed between said first surfaces and said at least one other button of the array of buttons compressed between said second surfaces form at least one coaxial transmission line.

Claim 18. (Original) The package of claim 17, wherein the at least one coaxial transmission line supports a transverse electro-magnetic mode of propagation across a wide frequency band.

Claim 19. (Original) The package of claim 17, wherein the at least one coaxial transmission line provides a predetermined impedance across a frequency band.

Claim 20. (Original) The package of claim 19, wherein the predetermined impedance is between about 50 Ohms and 100 Ohms.

Claim 21. (Original) The package of claim 19, wherein the wide frequency band is about 3 GHz wide.

Claim 22. (Original) The package of claim 11, wherein the array of buttons is arranged in a regularly spaced array.

Claim 23. (Original) The package of claim 11, wherein the array of buttons is arranged in a custom arrangement based upon positions of the contacts on one of the chip carrier and the printed circuit board.

Claim 24. (Original) The package of claim 11, wherein a rate at which the radius of at least one button of the buttons expands while being axially compressed slows at a first range of axial compression.

Claim 25. (Original) The package of claim 24, wherein the rate at which the radius of the at least one button expands while being axially compressed slows at a second range of axial compression.

Claim 26. (Original) The package of claim 11, wherein at least one button of the buttons comprise an elastomeric compound having embedded metallic particles.

Claim 27. (Original) The package of claim 11, wherein the buttons comprise a deformable material.

Claim 28. (Original) The package of claim 27, wherein the buttons comprise a deformable metal.

Claim 29. (Original) The package of claim 28, wherein the buttons comprise soft metal balls.

Claim 30. (Original) The package of claim 11, further comprising a clamping ring holding the chip against the interposer and the interposer against the printed circuit board.

Claim 31. (Original) The package of claim 11, wherein the chip comprises laminations that form said first surface and said second surface.

Claim 32. (Original) The package of claim 11, wherein the printed circuit board comprises laminations that form said first surface and said second surface.

Claim 33. (Original) A chip package comprising:
a chip carrier;

a printed circuit board;

an interposer having an array of buttons between said chip carrier and said printed circuit board; and

a sheet positioned between the interposer and one of the chip carrier and the printed circuit board,

wherein the sheet defines a first hole through which a first button of the array of buttons passes and a second hole through which a second button of the array of buttons passes, and

wherein the first hole compresses the first button more than the second hole compresses the second button.

Claim 34. (Original) The package of claim 33, wherein the second hole is large enough such that the second button does not contact the sheet.

Claim 35. (Original) The package of claim 33, wherein the first button is compressed such that the first button radially contacts an adjacent button.

Claim 36. (Original) A chip package comprising:

a chip carrier;

a printed circuit board;

an interposer having an array of buttons between said chip carrier and said printed circuit board; and

a sheet positioned between the interposer and one of the chip carrier and the printed

circuit board,

wherein the sheet defines a hole through which a first button of the array of buttons passes and comprises a conductive disc axially compressing a second button of the array of buttons.

Claim 37. (Original) The package of claim 36, wherein the second button is axially compressed such that the second button radially contacts an adjacent button.

Claim 38. (Currently amended) A method of making a chip package comprising:

providing an interposer having an array of buttons on a carrier having a proximity to each other that allows contact between two adjacent buttons to occur when the two adjacent buttons are compressed above a predetermined threshold; and

compressing the interposer between a chip carrier and a printed circuit board, wherein a rate at which the radius of at least one of the buttons expands while being compressed slows at a first range of axial compression.

Claim 39. (Original) A method of making a chip package comprising:

providing an interposer having an array of buttons;

providing a sheet having a hole and a conductive disc;

positioning the interposer between a chip carrier and a printed circuit board; and

positioning the sheet between the interposer and one of the chip carrier and the printed circuit board such that a first button of the array of buttons passes through the hole in the sheet and such that a second button of the array of buttons is axially compressed by the

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conductive disc.